

In re Application of: HATAKENAKA et al.

Application No. 09/871,978

Filed: June 1, 2001

For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE COMPRISING RAM WITH COMMAND DECODE SYSTEM AND LOGIC CIRCUIT INTEGRATED INTO A SINGLE CHIP AND TESTING METHOD OF THE RAM WITH COMMAND DECODE SYSTEM



Mail Stop Non-Fee Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

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JAN 02 2004

Technology Center 2100

- ☐ Small entity status is claimed for this application under 37 CFR 1.27.
- ☒ Petition for an extension of time for the period noted below, as well as for any additional period necessary to render the present submission timely. Please charge Deposit Account No. 12-1216 for the appropriate petition fee.
- ☒ Other: IEEE Standard Dictionary of Electrical and Electronic Terms (3 sheets)
- ☒ Please charge Deposit Account No. 12-1216 in the total amount indicated below. A duplicate copy of this transmittal sheet is enclosed herewith.

					SMALL ENTITY		OTHER THAN A SMALL ENTITY		
TIME EXTENSION PETITION FEE			none		\$ 0.00		\$ 0.00		
		subtract time extension fee previously paid		none		(\$ 0.00)		(\$ 0.00)	
CLAIM FEE		CLAIMS REMAINING AFTER AMENDMENT		HIGHEST NUMBER PREVIOUSLY PAID FOR	EXTRA CLAIMS PRESENT	RATE	ADDIT. CLAIM FEE	RATE	ADDIT. CLAIM FEE
TOTAL		18	MINUS	18	=00	x 9=	\$	x 18=	\$0
INDEPENDENT		4	MINUS	4	=00	x 43=	\$	x 86=	\$0
<input type="checkbox"/>	FIRST PRESENTATION OF MULTIPLE CLAIM					+ 145=	\$	+ 290=	\$
TOTAL AMOUNT TO BE CHARGED TO DEPOSIT ACCOUNT						TOTAL	\$	TOTAL	\$0

- ☒ The Commissioner is hereby authorized to charge any deficiencies in the following fees associated with this communication or credit any overpayment to Deposit Account No. 12-1216.
- ☒ Any filing fees under 37 CFR 1.16 for the presentation of extra claims.
- ☒ Any patent application processing fees under 37 CFR 1.17.

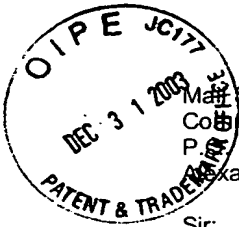
Respectfully submitted,

*Jeffrey A. Wyand*  
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Date: *December 31, 2003*  
JAW:nc

Amendment or ROA Transmittal (Revised 10/2/03)

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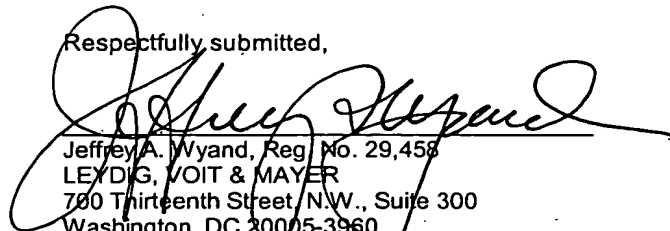
Technology Center 2100

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